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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/624,260	07/22/2003	Foot Shen Wong	02-0146	7340
	7590 12/03/200 HAO & BARNES, LLF	EXAMINER		
TWO NORTH	MARKET STREET, T	PARRIES, DRU M		
SAN JOSE, CA 95113			ART UNIT	PAPER NUMBER
			2836	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Application	on No.	Applicant(s)		
Office Action Summary		10/624,26	30	WONG ET AL.		
		Examiner		Art Unit		
		Dru M. Pa		2836		
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SH WHIC - Exter after - If NO - Failu Any r	ORTENED STATUTORY PERIOD FOR CHEVER IS LONGER, FROM THE MAIL asions of time may be available under the provisions of 3 SIX (6) MONTHS from the mailing date of this communic period for reply is specified above, the maximum statum to re to reply within the set or extended period for reply will, eply received by the Office later than three months after ad patent term adjustment. See 37 CFR 1.704(b).	LING DATE OF TH 7 CFR 1.136(a). In no everation. The period will apply and we by statute, cause the app	HIS COMMUNICATION ent, however, may a reply be tin ill expire SIX (6) MONTHS from lication to become ABANDONE	N. nely filed the mailing date of this communication. ED (35 U.S.C. § 133).		
Status						
1)🖂	Responsive to communication(s) filed of	on <u>05 November 2</u>	<u>007</u> .			
2a) <u></u> □	This action is <b>FINAL</b> . 2b)⊠ This action is non-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
	closed in accordance with the practice	under <i>Ex parte Qi</i>	layle, 1935 C.D. 11, 45	53 O.G. 213.		
Dispositi	on of Claims					
5)□ 6)⊠ 7)□	Claim(s) 1-19 is/are pending in the app 4a) Of the above claim(s) is/are v Claim(s) is/are allowed. Claim(s) 1-19 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction	withdrawn from co				
Applicati	on Papers					
10)	The specification is objected to by the E The drawing(s) filed on is/are: a) Applicant may not request that any objectio Replacement drawing sheet(s) including the The oath or declaration is objected to by	□ accepted or b)  n to the drawing(s) t  e correction is requir	be held in abeyance. See ed if the drawing(s) is ob	e 37 CFR 1.85(a). rjected to. See 37 CFR 1.121(d).		
Priority u	ınder 35 U.S.C. § 119					
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>						
Attachmen	• •		о <b>п</b>			
2) Notice	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO- mation Disclosure Statement(s) (PTO/SB/08) r No(s)/Mail Date <u>11/07</u> .	-948)	4) Interview Summary Paper No(s)/Mail Do 5) Notice of Informal P 6) Other:	ate		

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## **DETAILED ACTION**

1. Applicant's arguments with respect to claims 1, 4, 7, and 11 have been considered but are most in view of the new ground(s) of rejection.

## Claim Objections

2. Claim 19 is objected to because of the following informalities: the Examiner believes the phrase "crossover point" was meant to be "switchover point". The phrase "crossover point" was never mentioned before in any claim. Appropriate correction is required.

## Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- Claims 1-8 and 10-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Carobolante (6,084,378), Alfrey (2003/0103364), and Gay (6,791,390). Carobolante teaches a current source (V<sub>M</sub>) and four power switches forming an H-bridge circuit selectively coupled to supply current to a load. He also teaches a plurality of power switch driving circuits (not shown) to control the conduction state of the power switches to selectively couple at least two power switches to a PWM signal. Carobolante teaches two modes of operation: a linear mode for periods of low current consumption and a PWM mode for periods of higher current consumption. Also, the current associated with the PWM signal is zero during the time, just before switchover, while the linear current is being supplied. He also teaches the idea of adding additional circuitry to drive the load with linear current from the current source. Carobolante

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also teaches that the condition under which a PWM current or a linear current is used to power the load is a matter of design choice for one skilled in the art. Therefore, it is taught that any desired type of output condition (i.e. a specified ripple current), which is known to one of ordinary skill in the art, can be controlled and implemented by the linear current source and PWM signal of Carobolante's invention. (Col. 1, lines 30-31; Col. 2, lines 17-23, 29-31; Col. 3, lines 40-46; Col. 14, lines 18-20, 22-29; Fig. 1). Carobolante fails to explicitly teach the desired output producing a specified ripple current, a system having two current sources, the load being a thermal electrical cooler, what the direction of current through the load defines, and a controller. Alfrey teaches a linear H-Bridge circuit with for supplying current to a load, such as a thermoelectric cooler. He also teaches the direction of current through the load to define a cooling or heating mode. He also teaches the circuit comprising two current sources (Fig. 7, 7A; 17 & 19) with current source switches (601), wherein one source is coupled to the load during a first period and the other is coupled during a second period via current source switches ([0005] & [0044]). He also teaches a controller (21) to control switch driving circuits and the current source switch, wherein the controller comprises an input (22) representing the current to be flowing to the load. (Fig. 3A; [0033]) It would have been obvious to one of ordinary skill in the art at the time of the invention to implement a thermoelectric cooler as the load and define the direction of current as either a cooling or heating mode because some applications of an H-Bridge circuit are used specifically for thermoelectric coolers and the heating and cooling modes are necessary for the cooler to function properly (inherent). It also would have been obvious to one of ordinary skill in the art at the time of the invention to implement two current sources in the circuit to control the magnitude of the current being supplied to the load. It would have been

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obvious to one of ordinary skill in the art at the time of the invention to add Alfrey's controller to Carobolante's invention since Carobolante didn't explicitly state how his switch driving circuits were being controlled. Also, with the modifications of Alfrey into Carobolante's invention, the input (22) to the controller represents the switchover point based on the current to be supplied and that determines whether Carobolante's system will use the linear mode (low current) or the PWM mode (high current). Gay teaches a power distribution system including a semiconductor device that is formed to function as a voltage regulator (Abstract). He goes on to teach the voltage regulator operating over a wide range of load currents and operable to minimize ripple current in the output voltage (Col. 2, lines 32-42). Therefore, Gay teaches a system where a specific ripple current is achieved at all times (i.e. less than X amps; where X is greater than zero). It would have been obvious to one of ordinary skill in the art at the time of the invention to control the system of Carobolante to always produce an output voltage with a specified ripple current (less than X amps) to the load to minimize the possibility of malfunction and to provide a more precise output voltage.

5. Claim 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over Carobolante (6,084,378) and Alfrey (2003/0103364) as applied to claim 7 above, and further in view of Walter (2003/0155813). Carobolante and Alfrey teach an H-Bridge circuit as described above. The two references fail to teach a filter circuit coupled between some switches and the load. Walter teaches a filter circuit (34, 36) coupled between two of four switches and the load (Fig. 1; [0034]). It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate a filter circuit for attenuating harmonic distortion in the output voltage.

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## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dru M. Parries whose telephone number is (571) 272-8542. The examiner can normally be reached on Monday -Thursday from 9:00am to 6:00pm. The examiner can also be reached on alternate Fridays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry, can be reached on 571-272-2800 x 36. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DMP

11-19-2007

MICHAEL SHERRY
SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2800